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**RN-6827**

**B. E. III (Sem. V) (IC) Examination**

**May / June - 2010**

**Linear Integrated Ckts.**

Time : 3 Hours]

[Total Marks : 70

**Instruction :**

(1)

नीचे दृष्टाविले निशानीवाणी विगतो उत्तरवडी पर अवश्य लपवी. Fillup strictly the details of signs on your answer book.	Seat No. :
Name of the Examination :	<input type="text"/>
<input type="text" value="B. E. 3 (Sem. 5) (IC)"/>	<input type="text"/>
Name of the Subject :	<input type="text"/>
<input type="text" value="Linear Integrated Ckts."/>	<input type="text"/>
Subject Code No. : <input type="text" value="6"/> <input type="text" value="8"/> <input type="text" value="2"/> <input type="text" value="7"/>	<input type="text"/>
Section No. (1, 2,.....) : <input type="text" value="1&amp;2"/>	
Student's Signature	

**SECTION - I**

- 1 (a) Answer the following in brief : 10
- (i) What is an instrumentation amplifier? 1
  - (ii) What does the term balanced output mean? 1
  - (iii) Define the following electrical parameters : 2
    - (a) Input offset voltage
    - (b) Output voltage swing.
  - (iv) List the four negative feedback configurations. 2  
Which two configurations are most commonly used?
  - (v) What is thermal drift? How does it affect the 2  
performance of an Op-Amp?
  - (vi) What is an electrical noise? 1
  - (vii) Briefly explain the need of compensating 1  
networks in Op-Amps.
- (b) Explain the working of a 3-Op-Amp instrumentation 8  
amplifier with the help of a neat diagram.
- 2 (a) Explain the operation of an Op-Amp as 8
- (i) adder
  - (ii) subtractor.
- (b) Draw the ckt diagram of a non-inverting schmitt 8  
trigger and explain its operation with the help of  
hysteresis curve.

**OR**

- 2 (a) Explain Op-Amp as an integrator in detail. 8  
 (b) Explain the working of a sample and hold ckt. using Op-Amp. and also an inverting amplifier. 8
- 3 Attempt any two : 8×2=16  
 (i) Draw the block diagram of an Op-Amp. List the characteristics of an Op-Amp. Give typical values of the same for IC 741.  
 (ii) What is a slew rate? List causes of slew rate and explain its significance in applications.  
 (iii) What are the factors that affect the input offset voltage, input bias and input offset currents?
- 4 (a) Give answers in brief : 10  
 (i) What is the difference between summing and averaging ckt? 2  
 (ii) What is the function of level translator stage in Op-Amp. 2  
 (iii) Write the characteristics of an ideal Op-Amp. 2  
 (iv) Give two advantages of instrumentation amplifier. 2  
 (v) What is a differential amplifier? Which are its different configurations? 2  
 (b) Explain the working of an astable multivibrator using IC 555 with diagram and suitable expression. 8
- 5 (a) Explain second order high pass butterworth filter using Op-Amp. 8  
 (b) Explain digital to analog converter using R-2R ladder networks. 8

**OR**

- 5 (a) Define filter. Derive an expression for gain for a second order low pass filter. 8  
 (b) Explain the working of successive approximation type analog to digital converter. 8
- 6 Attempt any two : 8×2=16  
 (i) Explain with the help of a neat block diagram a voltage regulator using IC 7805. What is meant by 'Temperature Stability' and 'ripple rejection'?  
 (ii) Explain wein-bridge oscillator using Op-Amp. Design wein-bridge oscillator so that output frequency  $f_o = 1$  kHz.  
 (iii) Explain the working of full wave precision rectifier and discuss its advantages over conventional rectifier.